IV Year I Semester   **L   T   P   C**
Code: 17EC712   **0   0   3   2**

VLSI LABORATORY

Note: The students are required to design the schematic diagrams using CMOS logic and to draw thelayout diagrams to perform the following experiments using 130nm technology with the Industry standard EDA Tools.

List of Experiments:
1. Design and Implementation of an Universal Gates
2. Design and Implementation of an Inverter
3. Design and Implementation of Full Adder
4. Design and Implementation of Full Subtractor
5. Design and Implementation of Decoder
6. Design and Implementation of RS-Latch
7. Design and Implementation of D-Latch
8. Design and Implementation asynchronous counter
9. Design and Implementation of static RAM cell
10. Design and Implementation of 8 bit DAC using R-2R latter network
    Software Required:
    Mentor Graphics Software / Equivalent Industry Standard Software.
    Personal computer system with necessary software to run the programs and to implement.