Mr. Y.E.Vasanth Kumar

Assistant Professor

Department of Electronics & communication EngineeringRaghuEngineeringCollege,Visakhapatnam,Andhra PradeshINDIA

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Interests: Electronic devices & circuit, Digital Electronics, Radar Engineering, Antennas wave propagation

CourseTaughtPreviously:

PG.
• Advanced Radar Engineering
• Advanced digital system design

UG.
• Electronic devices & circuit
• Pulse and Digital Circuits
• Digital Electronics
• Linear IC Applications
• Industrial Electronics
• Microwave Engineering
• Antennas & Wave propagation
• Information theory coding
• Radar Engineering
• Cellular Mobile Communication
• Data Communications
• Analog Electronics circuits Lab
• ElectronicDevicesandcircuitsLab
• DigitalElectronicsLab
• Digital Communication Lab
• PulseandDigitalCircuitsLab

Publications:

InternationalJournals:

1. Published a Paper On "High Speed Energy Efficient Clock-Less Approach for Designing Asynchronous Architectures" IJIRCCE (ISSN No: 2320-9801) Vol. 4, Issue 1, January 2016.
2. Published a Paper On "Energy Efficient Multiplier design using Multigate MOSFET'S" IJIRSET (ISSN No: 2319-8753) Vol. 5, Issue 2, February 2016.
3. Published a Paper On "High Speed Energy Efficient Clock-Less Approach for Designing Asynchronous Architectures" IJIRCCE (ISSN No: 2320-9801) Vol. 4, Issue 1, January 2016.
4. Published a paper on "Design of an 8-bit Magnitude Comparator with Small Transistor Count and Low Power Using Hybrid STATIC CMOS/ TG Logic'', IJARCET (ISSN No. 2278- 1323) Volume 6, Issue 9, Sept 2017.
5. Published a paper on "Enhanced adiabatic paradigm for Ultra Low Power and high speed Switching'', IJRASET (ISSN No. 2321-9653), Volume 6, Issue IV, April 2018.
6. Published a paper on "Circularly Polarized Microstrip Patch Antenna Using PBG Structure'', IJITEE (ISSN No. 2278-3075), Volume 8, Issue 6, April 2019.
7. Published a paper on "Optimized VLSI design of 2 bit magnitude comparator using GDI Comparator'', IJRTE (ISSN No. 2277-3878), Volume 7, Issue 6, March 2019.
8. Published a paper on "Novel Design of Thz Microstrip Patch Antenna for Radar Application '', IJITEE (ISSN No. 2278-3075), Volume 8, Issue 6, April 2019.
9. Published a paper on "An Efficient High Speed GDI Dadda Multiplier'', IJRTE (ISSN No. 2277-3878), Volume 8, Issue 1, May 2019.
10. Published a paper on "Design of Monopole Textile U-Slot Antenna for ISM Band and Wi-Fi Applications", IJAST (ISSN No. 2005-4238), Volume 29, Issue 4, June 2020.
11. Published a paper on "Design of a Dual Shaped E Textile Antenna at 5GHz'', IJAST (ISSN No. 2005-4238), Volume 29, Issue 4, June 2020.
12. Published a paper on "Design of a low specific absorption rate on-off body circular slotted textile antenna for medical applications" NeuroQuantology (ISSN: 1303-5150), Volume 20, Issue 10, August 2022, Pg-8638-8646.
13. Published a paper on "A multi slotted rhombus shaped flexible antenna with DGS for biomedical applications" NeuroQuantology (ISSN: 1303-5150), Volume 20, Issue 11, September 2022, Pg- 4205-4210.
14. Published a paper on "A Multi Stub Polyon Shaped textile antenna for deformation performance using a periodic DGS'', IJEER (ISSN No. 2347-470X), Volume 10, Issue 4, December 2022, Pg- 1233-1238.

Conference Proceedings:

1 Participated in IEEE conference in Advanced in communication, navigation and computer networks (ACNCN-2012) organized by Dept. of ECE in AUCE on"Image encryption using binary parity check method''.
2 Presented a paper titled "Design and Analysis of LOL-P Textile Antenna" at 10th Innovation in Bio-Inspired Computing and Applications (IBICA- 2019) international Conference at GIET University, Gunupur, Odisha, India in Springer Nature conference in "Advances in Intelligent Systems and Computing".
3 Presented a paper titled "Design of a Dual Ring Resonator Antenna for WBAN Applications" in International Conference on Smart & Sustainable Technologies (ICSST-2021) at GIET University, Gunupur, and Odisha, India in Springer Nature conference during December 16-18, 2021.

Seminars,Workshops,FDPsandSTTPs:

1 Participated in four days online workshop on "ANALOG & DIGITAL IC DESIGN using cadence design flow" , during 26th & 29th Oct 2022 organized by ECE, REC association with Entuple Technologies Pvt. Ltd, Hyderabad.
2 Participated in two days online workshop on "Embedded System Design on 8 bit Microcontroller Using KEIL, during 29th & 30th may 2020 organized by ECE, ANITS.
3 Participated in faculty workshop on CRUX of writing journal paper, research project proposal and patents on 27th – 28th April 2017, organized by department of MECH – RAGHU Engineering College.
4 Participated in workshop on Basics of MATLAB on 19th – 20th April 2017, organized by department of ECE – RAGHU Engineering College.
5 Participated in National Workshop on Advanced Antenna Design (NWAAD 2K16)
   on 19th - 21st October 2016, organized by department of ECE Andhra University – AUCE.
6 Participated in workshop on latest technology in EC (WOLTEC-II), 2012 at MVGR College of engineering.

FDP – Faculty Development Programs

1 Participated in online FDP on "Artificial Intelligence Machine Learning and Deep Learning with Matlab and Simulink" from 23rd to 25th Nov 2022 organized by ECE, REC in association with Capricot Technologies Pvt. Ltd, Hyderabad.
2 Participated in online E&ICT Academy recognized FDP on "Machine Learning in Real Time Embedded & IOT Applications" from 2th to 12th May 2022 organized by ECE, NIT, Warangal.
3 Participated in online AICTE recognized FDP on "Antenna Design Techniques and Software Simulation using HFSS" from 13th to 17th July 2020 organized by ECE, NITTTR.
4 Participated in online AICTE recognized FDP on "Antenna Design Techniques and Software Simulation using HFSS" from 13th to 17th July 2020 organized by ECE, NITTTR.
5 Participated in one week online FDP on "Advanced Research Methodology and Innovation Teaching Pedagogy" organized by TGPCET & IoE, ISTE Nagpur from 8th to 13th June 2020.
6 Participated in three days FDP on "VLSI Technology and Hardware for AI & ML" organized by ECE, GMRIT during 25th may to 27th may 2020.
7 Participated in one week FDP on "Advanced Antenna Design Using HFSS" organized by ECE, SEC& VCE from 12th may to 17th may 2020.
8 Participated in 5 days FDP on SCILAB from 29^{th}april to 3rd may 2020 organized by IIT Bombay-PMMMNMTT.

9   Participated in FDP on "Advances in Practical Antenna design" organized by the E&ICT Academy ,NIT, Warangal at department of ECE – RAGHU Engineering College from 25th – 30th May 2017.

10 Participated in FDP on "Advances in Practical Antenna design" organized by the E&ICT Academy ,NIT, Warangal at department of ECE – RAGHU Engineering College from 25th – 30th May 2017.

Online Courses:

1. Successfully completed CMOS Digital VLSI Design- (Feb to Apr 2019), 8 Weeks Course with FDP Roorkee.

2. Successfully completed Fiber Optics – (Feb to March 2018), 8 Weeks Course conducted by IIT Roorkee.

3. Successfully completed Microwave Integrated Circuits-(Jul to Sep 2017), 8 Weeks Course conducted by IIT Bombay.

Additional Responsibility:

1. Class coordinator,
2. Training &placement coordinator,
3. NBA, and NAAC Criteria related works

Memberin:

1. IAENG

2. IEEE