Dr. Anusha Gorantla

Associate Professor

Department of Electronics & communication Engineering

Raghu Engineering College, Visakhapatnam - 531162, Andhra Pradesh, INDIA

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Interests: Low Power VLSI; Approximate computing; High-Performance computing; Image processing;

Course Taught Previously

PG.
- Digital system design using HDL
- Low Power VLSI Design

UG.
- Digital Electronics
- Verilog HDL
- Digital Communication.
- Electronic Devices and circuits
- Signals and Systems
- Control systems
- Optical communication

Laboratories Handled
- VLSI Lab.
- Digital Electronics lab
- DICA (Digital integrated circuits and applications) Lab
- Digital Communication Lab
- Digital system design Lab

Publications

International Journals

1.   Anusha Gorantla and P. Deepa, "Design of Approximate adders and multipliers for error tolerant image processing", **Journal of Microprocessors and Microsystems -Elsevier** January 2020
DoI: https://doi.org/10.1016/j.micpro.2019.102940 [**SCI, Elsevier, Impact factor: 1.045**]
2.   Anusha Gorantla and P. Deepa, "Design of approximate subtractors and dividers for error tolerant image processing applications**", Journal of Electronic testing (JETTA) -Springer** October 2019
DoI: https://doi.org/10.1007/s10836-019-05837-5 [**SCI, Springer, Impact factor: 0.625**]
3.   Anusha Gorantla and P Deepa, "Design of approximate compressors for multiplication", **ACM Journal on emerging technologies in computing systems (JETC)** Vol.13, issue 3, May 2017
 DoI: 10.1145/3007649 [SCI, ACM, Impact factor:1.36]
4.   Anusha Gorantla and P. Deepa, "Design of Energy Efficient approximate adders for high performance computing applications", **International Journal of Applied Engineering Research** Vol.10, No.10, 2015, pp. 9865-9870
5.   Grande Nagajyothi and G.Anusha, Debanjan Kunda "Low power design of 2-4 and 4-16 Line Decoders" **International Journal of Innovative technology and Exploring Engineering (IJTEE)** 2019

Conference Proceedings

1.   Anusha Gorantla, Roopa K and T L Spandana , "Developments of Approximate Computing from Algorithm Level to System Level ", ICP3 2022, Raghu Institute of Technology, Visakhapatnam, JAN- 7 and 8 2022. (IEEE)
2.   Grande Nagajyothi,G.Anusha, and Debanjan Kunda, "ASIC Implementation of Fixed point iterative parallel and pipeline CORDIC Algorithm", SOCPRO 2019. (Springer)
3.   Grande Nagajyothi, G.Anusha, N Dilip kumar and Debanjan Kunda, "Design of FINFET based DRAM cell for low power applications", CADEC 2019. (Taylor and Francis)
4.   Anusha Gorantla and P Deepa, "Design of Energy Efficient Approximate Adders for high performance computing applications" ISREA 2015.

PHDs Supervised -Nil

Workshops/Conferences/Seminars/Symposiums organised

Conferences/Symposiums -NIL

Seminars, Workshops and STTPs

S. No	Name of the Programme	Dates	Sponsored by
1.	Machine learning in Real Time Embedded &IOT Applications	2nd -12th May 2022	Electronics & ICT Academy NIT Warangal
2.	Topics in Hardware Security	27-31 December 2021	ATAL Academy- IIT Ropar
3.	Internet of Things (IoT)	14-18 December 2020	ATAL Academy- KL University, Vijayawada
4.	Quantum Computing	7-12 December 2020	ATAL Academy- Muthayammal Engineering College (Autonomous) Namakkal.

Visit to outside institute/organization: NIL

Continuing Education / QIP Short Term Lectures / Special Lectures given: NIL

Details of professional training and research experience

S.No.	Title of the Course	Place	Year	Duration
1	Design for Testability (DFT)	Bangalore	2022	4 months

Projects
Sponsored R & D, Consultancy Projects: NIL

Awards and Honors : NIL
Additional Responsibility

RESPONSIBILITIES WITHIN THE INSTITUTE:

1. BOS internal member of ECE Department for curriculum development at Raghu Engineering College.
2. NBA ECE Department criteria 4 co-ordinator at Raghu Engineering College.
3. NAAC ECE Department criteria 4 co-ordinator at Raghu Engineering college.
4. Member of women grievance cell at Raghu Engineering College.
5. Member of ECE R&D cell at Raghu Engineering College.

POSITIONS HELD OUTSIDE THE INSTITUTE:

1. Member in publon
2. Reviewer in IEEE Access
3. Reviewer in International Journal of Speech Technology -Springer