Mr.K.Chandra Sekhar, M.Tech, (Ph.D)

Assistant Professor

Department of Electronics & Communication Engineering,

Raghu Engineering College,Dakamarri-531162, Andhra Pradesh , India

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Area of Interest : VLSI, Communications.

Academic Achievements

- Qualified as Assistant Professors in National Eligibility Test (NET) in the Year 2015 & 2016

- Qualified for Graduate Aptitude Test in Engineering (GATE) in the Year 2010, 2014 & 2017.

Subjects Taught Previously

- **Data Communications (DC)**
- **Electro Magnetic Waves & Transmission Lines (EMWTL)**
- **Digital Logic Design (DLD)**
- **Digital Communications (DC)**
- **Radar Systems (RS)**
- **Cellular & Mobile Communications (CMC)**
- **Control Systems (CS)**
- **Computer Networks (CN)**
- **Electronic Devices & Circuits (EDC)**
- **Embedded Systems (ES)**
- **Wireless Sensor Networks (WSN)**
- **Signals & Systems (SS)**

Laboratories Handled

- **Digital IC Design**
- **Digital Communications**
- **Analog Communications**
- **VLSI**
- **Digital Electronics**
- **Electronic Devices & Circuits**
- **Signals & Systems (MATLAB)**
- **Microwave**

International Journals

1. **Published a paper on" An Efficient Pseudo Random Generator For Cryptographic Applications" in IJEAT VOL 4 ISSUE 1.**

2. **Published a paper on" Design & Implementation of Pseudo Random Generator in FPGA and CMOS VLSI " in IJCSIT VOL 6 ISSUE 6.**

3. **Published a paper on" A Novel approach to implement a Vedic Multiplier for High Speed Applications " in IJSETR VOL 4 ISSUE 44.**

4. **Published a paper on" Design & Implementation of High Speed MAC Unit using Carry Select Adder " in IJVDCS VOL 4 ISSUE 4.**

5. **Published a paper on" Design of PIPO Shift Register using Clocked Pass Transistor Logic   " in IJAREEIE VOL 5 ISSUE 7.**

6. **Published a paper on" Design and Implementation of UTMI with USB 2.0 Specifications " in IJIRSET VOL 6 ISSUE 10.**

7. **Published a paper on" Multi Purpose Agribot " in IJSETR VOL 7 ISSUE 03.**

8. **Presented a Paper on " A Novel Approach to Control Leakage Power in CMOS Circuits in " International Journal of Science Engineering and Mathematics" organized b BABA Engineering College, VIZAG.**

9. **Published a paper on" IOT Based Home Automation System " in IJMTE VOL 9 ISSUE 03.**

10. **Published a paper on" IOT Based Smart Irrigation System " in IJRECE VOL 7 ISSUE 01.**

11. **Published a paper on" Node Failure Detection Mechanisms in Wireless Sensor Networks " in AEGAEUM   VOL 8 ISSUE 04.**

12. **Published a paper on" IOT Based Air & Sound Pollution Monitoring System " in AEGAEUM VOL 8 ISSUE 05.**

13. Published a paper on" Design and Implementation of QPSK Modulator & De-Modulator " in AEGAEUM VOL 8 ISSUE 05.

14. Published a paper on" Design of an Efficient RF Energy Harvesting Multiband Rectenna" in JETIR ,JULY,2021.

15. Published a paper on" Design of ALU using Ternary Logic " in IJCRT ,July 2021.

16. Published a paper on" Performance Analysis of BPSK, QPSK & QAM over AWGN Channel " in IOSR, June 2022.

17. Published a paper on" Deep Learning Based detection of Covid-19 " in CEJ, June 2022.

18. Published a paper on" IoT Based Smart Monitoring Cradle " in JETIR, Jan 2023.

19. Published a paper on" Efficient decoding of Low Density Parity Check Codes" in IJSREM ,May 2023.

20. Published a paper on" Securing Data Through Steganography & Secret Sharing Schemes " in IJIRT, MAY 2023.

FDPS/WORKSHOPS

1. Attended the two-day National work-shop on "VLSI Design using Mentor graphics tools " organized by M.V.G.R COE, Vizianagaram.

2. Attended the two-day National work-shop on "Communications , Signal and
3. Image Processing " organized by JNTU-K, Vizianagaram.

4. Attended One Week Short term Course on "Analog and Mixed Signal Circuit Design " using Mentor Graphics Tools organized by MVGR COE, Vizianagaram.

5. Attended the two-day National work-shop on "Analog and Digital IC Design using Mentor graphics tools " organized by Sri Vishnu Engineering College for Women, Bhimavaram.

6. Attended One week Faculty Development Programme (FDP) on "Advanced Digital Signal Processing " organized by E & ICT Academy, NIT Warangal at Department of ECE , M.V.G.R COE, Vizianagaram.

7. Attended One week Faculty Development Programme (FDP) on "Advances in Practical Antenna Design" organized by E & ICT Academy, NIT Warangal at Department of ECE Raghu Engineering College , Visakapatnam.

8. Attended One week Faculty Development Programme (FDP) on "Analog Cmos IC Design " organized by E & ICT Academy, NIT Warangal at Department of ECE, VRK Siddharta Engineering College , Vijayawada.

9. Received Faculty Development Programme (FDP) Certificate for "Switching Theory and Logic Circuits " organized by NPTEL Credit Course.

10. Received   Faculty Development Programme (FDP) Certificate for "Teaching and Learning in Engineering (TALE) " organized by NPTEL Credit Course .

11. Attended   One week Faculty Development Programme (FDP) on "Real Time Embedded Systems and IOT, its Applications, organized by NIT Warangal at Department of ECE, Raghu Institute of Technology , Visakhapatnam.

12. Attended Three days Online Faculty Development Programme (FDP) on "Learn ARDUINO UNO Step by Step" , organized by GMRIT, Department of ECE,   Rajam.

13. Attended One week online Faculty Development Programme (FDP) on "Teaching, Learning & Assessment Strategies organized by IEEE Hyderabad Section and IEEE Education Society.

14. Attended   Four days Online Faculty Development Programme (FDP) on "Design of Drones" , organized by Srinidhi Educational Groups, Hyderabad.

15. Attended   Two days online Faculty Development Programme (FDP) on "Embedded system design on 8 Bit Micro-controller using KEIL" , organized by ANITS   , Visakhapatnam.

Responsibilities with the Institute

- **CRT Cordinator**
- **NBA Crtieria 7, 9 cordinator**
- **SMILES**

Member Ships

- **IRED**
- **ISTE**
- **IAENG**