



Mr. Srinivasa Gupta Noolu

Assistant Professor

Department of Electronics & Communication Engineering
Raghu Engineering College, Visakhapatnam, Andhra Pradesh
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Interests: VLSI, Low Power

Courses Taught Previously:

UG:

- Switching Theory & Logic Design
- Digital IC Applications
- Microprocessors & Microcontrollers
- Electronic Circuit Analysis
- Control Systems
- Probability Theory & Stochastic Processes
- Signals & Systems
- Digital Signal Processing
- Linear IC Applications
- Satellite Communications
- Radar Systems
- Microwave Engineering
- Electronic Measurements & Instrumentation

Laboratories Handled:

- ECAD Lab
- DSP Lab
- Analog Communications Lab
- EDC Lab
- Signals & Systems Lab
- Digital Logic Design Lab

Publications :

International Journals:

1. N. Srinivasa Gupta et. al., “Detection of Plant Leaf Diseases Using Random Forest Classifier”, IJIRT, Volume 9, Issue 1, pp1300-1302, June 2022
2. N. Srinivasa Gupta et. al., “Effective Face Detection Over Random Motion, Blur and Pose”, IJIRT, Volume 9, Issue 1, pp1096-1100, June 2022
3. S.B.G. Tilak Babu , V. Satyanarayana , N. Srinivasa Gupta , “Image Super Resolution by Interpolation and Edge Modification”, JARDCS, Volume 10, 14-Special Issue, pp1953-1956, November 2018
4. N. Srinivasa Gupta et. al., “Smart System for Rider Safety and Accident Detection”, IJERT, Volume 9, Issue 6, pp154-157, June 2020
5. N. Srinivasa Gupta et. al., “A novel domino logic for arithmetic circuits”, IJITEE, Volume 3, Issue 3, pp34-37, August-2013

Conference Proceedings:

1. N. Srinivasa Gupta, J. Lavanya, P. Kusuma Vani, “Maintenance Scheduling of Heavy Machinery using IoT for wide range of Realtime applications”, 5th International Conference on Micro-Electronics, Electromagnetics & Telecommunication”, ICMEET- 2019, 331-340 December, Raghu Institute of Technology, Visakhapatnam, Andhra Pradesh. (Springer-LNEE Series)

Seminars, Workshops, FDPs and STTPs:

S. No.	Name of the Programme	Date	Conducted by
1	VLSI Technology trends and use cases in advanced applications	16/12/2024 to 26/12/2024	Mallareddy Engg College(A), Hyd in association with NIT, Warangal
2	Outlook on Best Practices and strategies for Chip Design Implementation-From Industry Perspective	16/12/2024 to 21/12/2024	GVPC for Degree & PG Courses under ATAL FDP
3	Advances in Wireless Communications and Antenna Technologies for Space, Vehicular and Ground Applications	12th June, 2023	Raghu Institute of Technology (A) and Raghu Engineering College (A), Dakamarri, Visakhapatnam
4	Current trends in AI & Engineering Applications in the area of ML and DL	6/02/2023 to 10/02/2023	JNTU-GV
5	5-day Online FDP on the theme “Inculcating Universal Human Values in Technical Education	7/11/2022 to 11/11/2022	AICTE

Awards and Honors:

1. Qualified in UGC NET-2014(June) for Lectureship.
2. Secured *state 6th* rank in APRCET-2019.
3. Secured *state 3rd* rank in PGECET-2011.
4. Qualified in GATE-2011.

Additional Responsibilities:

Class coordinator, Training& placement coordinator, NBA Criteria related works.

Member In:

IAENG: (Member Number: 259853)

ICSES: (ID: 16784)

Online Course Certification's:

S.No.	Course	Duration	Year	Course offered by	Score %
1	VLSI Design Flow: RTL to GDS	12 weeks	October 2023	NPTEL-AICTE	84%
2	System Design through Verilog	8 weeks	October 2021	NPTEL-AICTE	97%
3	Digital Circuits	12 weeks	October 2019	NPTEL	81%
4	Switching Circuits & Logic Design	12 weeks	October 2018	NPTEL-AICTE	90%
5	CMOS Digital VLSI Design	8 weeks	March 2022	NPTEL	71%
6	Control Engineering	12 weeks	April 2018	NPTEL	50%