



Mr. Palli Sekhar Babu

Assistant Professor

Department of Electronics & communication Engineering

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Interests: Mixed Signal Simulation, Low Power VLSI

Course Taught Previously

UG

- VLSI Design
- Digital Communications
- Digital Electronics
- Digital IC Applications

Laboratories Handled

- VLSI Design
- Analog Communications
- Digital Communications
- Digital Electronics
- Linear IC Applications
- Pulse and Digital Circuits
- Digital IC Applications
- Electronic Devices and Circuits

Publications

International Journals

- Sekharababu Palli, “Design of Hybrid Logic 4-Bit Comparator With Efficient VLSI Design Constraints” JSJ.U Volume-16 Issue-10, October 2020.
- Sekharababu Palli, “CPW –FED TEXTILE Antenna for Wearable Wireless Communication Applications” IJAASR Volume-5 Issue-2 , 2020
- Sekharababu Palli “Algorithm to Image Identification and Artix-7 FPGA Implementation for Adequate Edge Detection” IJRTE Volume-8 Issue-5, January 2020.
- Sekharababu Palli “Typical Implementation of VITERBI Decoder for efficient error detection and correction” IOSR, Volume 8, Issue 5, May 2018.

- Sekharababu P, Shaik Mastan Vali, “Fast Fourier Transform Pipeline Architecture for OFDM”, IJAREEIE, Volume 3, Issue 11, November 2014.

Additional Responsibility

Responsibilities within the Institute:

1. Department NSS Coordinator since 2019
2. Department Sports Coordinator
3. Member, NAAC Criteria 3.6.4 (Department level)