



H K RAGHU VAMSI KUDULLA

Assistant Professor

Department of Electronics & communication Engineering

Raghu Engineering College, Dakamarri, Visakhapatnam-531162

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Interests: Digital circuits, Low Power VLSI, CMOS Logic design, Approximate Computing

Course Taught Previously

UG.

- Digital Electronics
- Digital Logic Design
- VLSI Design
- Digital IC Applications
- Electronic Measurements and Instrumentation.
- Electronics Devices and Circuits
- Electronics Circuit Analysis
- Network Analysis
- Radar Systems
- Analog Communication
- Digital Communication
- Pulse and Digital Circuits
- Linear and Digital IC Applications

Laboratories Handled

- Pulse and Digital Circuits Lab
- Digital IC Applications Lab
- Electronics Devices and Circuits Lab
- Digital Electronics Lab
- Linear IC Applications Lab
- Analog Communications Lab
- Digital Communications Lab

Publications

International Journals

- Published a journal on “**Design of Partial Products Partitioning Adder with High Speed and Low Power Delay Product**”, Suranaree Journal of Science & Technology, ISSN No (Online):0858-849X, Volume 29, Issue 5, 2022.
- Published a journal on “**An Efficient Test sets for analysis of Full Adder Circuit**”, The International journal of analytical and experimental modal analysis (IJAEMA) ISSN No (Online):0886-9367 Volume 13, Issue 9, 2021.
- Published a journal on “ **CPW-Fed Textile Antenna for Wearable Wireless Communication Applications**”, International Journal of Applied and Advanced Scientific Research (IJAASR),ISSN (Online): 2456 - 3080 Volume 5, Issue 2, 2020
- Published a journal on “ **Algorithm to Image Identification and Artix-7 FPGA Implementation for Adequate Edge Detection**”, International Journal of Recent Technology and Engineering (IJRTE) ISSN: 2277-3878, Volume-8 Issue-5, January 2020.
- Published a journal on “**Full Adder Designs Using Low Power Full Swing XOR and XNOR Structures**”, International Journal of Innovative Technology and Exploring Engineering (IJITEE)’, ISSN: 2278–3075 (Online), Volume-8 Issue-8, June 2019, Page No.:198-203
- Published a journal on “**Design and implementation of UWB systems with timing synchronization in MATLAB Simulink**” e-ISSN: 2278-2834, p- ISSN: 2278-8735.Volume 11, Issue 4, Ver. II (Jul.-Aug .2016)
- Published a journal on “**Reduction in Dynamic Power of Digital Circuits by Guarded Evaluation**”, IRJET, Volume: 03 Issue: 12 | Dec -2016, e-ISSN: 2395 - 0056
- Published a journal on “**Low Power Optimization of Full adder, 4-Bit Adder And 4-Bit BCD Adder**”, International Journal of Scientific & Technology Research Volume 2, Issue 9, September 2013 ISSN: 2277-8616.

Conference Proceedings:

- **Y. Sudha Vani, H K Raghu Vamsi, S. Srinivasa Rao**, “Design of Three Stage Dynamic Comparator with Tail Transistor using 20nm FinFET Technology for ADCs” *International Conference on Computing, Communication and Power Technology Organized by Raghu Institute of Technology and Raghu Engineering College Visakhapatnam* in technical collaboration with IEEE Vizag Bay Section, 7-8 January, 2022.

PHDs Supervised: NIL

Workshops/Conferences/Seminars/Symposiums organised: NIL

Visit to outside institute/organization: NIL

Continuing Education / QIP Short Term Lectures / Special Lectures given: NIL

Details of professional training and research experience

National / International	Topic, Venue	Date and Year
Conducted by Other Organizations		
Raghu Engineering College		
NATIONAL	“Advances in Practical Antenna Design” by E & ICT Academy, NIT Warangal	25 th – 30 th May 2017.
NATIONAL	5G Wireless Communications, Organized by E & ICT Academy, NIT Warangal	5 th - 10 th January 2018.
NATIONAL	Real Time Embedded Systems and IoT, its Applications” by E & ICT Academy, NIT Warangal	2 nd - 8 th June 2018.
NATIONAL	Recent Trends in Communication Engineering, Sponsored by AICTE, Organized by UCEK, Kakinada.	4 th -9 th February 2019.
NATIONAL (online)	Emerging Technologies in Electronics & Communication Engineering, Organized by SVEC, Tirupathi, Sponsored by IETE.	18 th -22 th May 2020.
NATIONAL (online)	Speech processing Applications & Challenges, Organized by Santhiram Engineering College, Nandyal.	20 th -25 th May 2020.
NATIONAL (online)	Workshop on Communication, VLSI, Image Processing and Design of Antennas.	3 rd - 4 th September 2020.
NATIONAL (online)	Control Systems & Sensors Technology conducted by ATAL Academy at Gayatri Vidya Parishad College for Degree and PG courses (A).	2 nd – 6 th November 2020.
NATIONAL (online)	“Recent advances in IoT and Machine Learning: Application and Hands-On” Organized by Raghu Engineering College, Visakhapatnam.	16 th -18 th December 2021.
NATIONAL (online)	Advanced Signal Processing, Communications Using AI & ML Techniques Organized by GMR Institute of Technology and Sairam Institute of Technology.	21 st -25 th March 2022.
NATIONAL (Online)	“Machine Learning in Real Time Embedded & IOT Applications” by NIT Warangal & REC	2 nd – 12 th May 2022

Projects Sponsored R & D, Consultancy Projects: NIL

Awards and Honors

- I got the Elite-silver in NPTEL Course- Digital Circuits.
- I got Elite Certificate in NPTEL online course- Principles of Communication Systems-1.

Additional Responsibility

RESPONSIBILITIES WITHIN THE INSTITUTE:

- NAAC Criteria-6.
- Department NPTEL Co-ordinator.
- Class In charge.
- Exam cell duties.
- NBA Criteria-6.

POSITIONS HELD OUTSIDE THE INSTITUTE:

- Class In-charge of 2017 Batch students in ECE department, Aditya Engineering College.
- Department Placement Co-ordinator of 2017 Batch students in ECE department, Aditya Engineering College.
- Department Exam Cell In-charge in Kaushik College of Engineering in the Academic year 2014-15.