



RAGHU ENGINEERING COLLEGE (AUTONOMOUS)

(Approved by AICTE, New Delhi, Accredited by NBA (CIV, ECE, MECH & CSE), NAAC with 'A+' grade
& Permanently Affiliated to JNTU-Gurajada, Vizianagaram)

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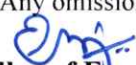
II B.TECH II SEMESTER (AR 20) SUPPLEMENTARY EXAMINATIONS, MARCH 2025

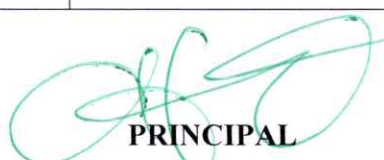
TIME TABLE

Timings: 01:00PM to 04:00PM

Branch	24.03.2025	25.03.2025	26.03.2025	27.03.2025	28.03.2025
CE	OOPS Through Java 20ES4009	Complex Variables Statistical Methods 20MA4007	Structural Analysis - I 20CE4006	Hydraulics and Hydraulic Machines 20CE4007	Transportation Engineering 20CE4008
EEE	Digital Logic Design 20ES4006	Synchronous and Asynchronous Machines 20EE4005	Control Systems 20EE4006	Power Systems - I 20EE4007	Managerial Economics and Financial Accountancy 20HS4002
ME	OOPS Through Java 20ES4009	Fluid Mechanics and Hydraulic Machinery 20ME4004	Metal Cutting and Machine Tools 20ME4005	Thermal Engineering 20ME4007	Design of Machine Members- I 20ME4006
ECE	OOPS Through Java 20ES4009	Analog Electronic Circuits 20EC4005	Signals & Systems 20EC4006	Probability Theory and Stochastic Processes 20EC4007	Managerial Economics and Financial Accountancy 20HS4002
CSE	Object Oriented Programming through Java 20ES4009	Probability and Statistics 20MA4006	Compiler Design 20CS4009	Operating Systems 20CS4006	Managerial Economics and Financial Accountancy 20HS4002
CSM	Object Oriented Programming through Java 20ES4009	Probability and Statistics 20MA4006	Compiler Design 20CS4009	Artificial Intelligence 20AI4011	Managerial Economics and Financial Accountancy 20HS4002
CSD	Object Oriented Programming through Java 20ES4009	Probability and Statistics 20MA4006	Compiler Design 20CS4009	Artificial Intelligence 20AI4011	Managerial Economics and Financial Accountancy 20HS4002
CSC	Object Oriented Programming through Java 20ES4009	Probability and Statistics 20MA4006	Compiler Design 20CS4009	Computer Networks 20CS4012	Managerial Economics and Financial Accountancy 20HS4002
CSO	Object Oriented Programming through Java 20ES4009	Probability and Statistics 20MA4006	Compiler Design 20CS4009	Micro Processors and Microcontrollers Foundations 20IO4010	Managerial Economics and Financial Accountancy 20HS4002

- Exam Halls – Room Numbers will be displayed in department notice boards.
- Any omissions or clashes in the “Time Table” may please be informed to the Controller of Examinations immediately.


Controller of Examinations


PRINCIPAL